

1200V 32mohm Silicon Carbide Power MOSFET

AK1CK2M032WAMH-A

Features:

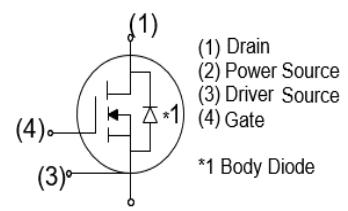
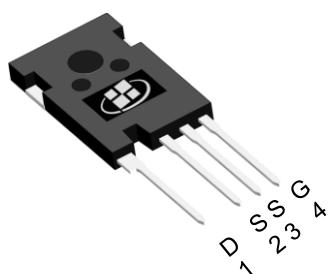
- Low on-resistance
- Fast switching speed with low capacitances
- Fast intrinsic diode with low reverse recovery (Q_{rr})
- Halogen-free, RoHS compliant
- AEC-Q101 Qualified

Applications:

- Motor drives
- DC/DC converters
- Switch mode power supplies
- Solar inverters

Key Performance Parameters:

Parameter	Value	Unit
V_{DS}	1200	V
$R_{DS(ON,\ TYP)}$ @ $V_{GS} = 18\ V$	27	mΩ
I_D	80	A
P_D	348	W



Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AK1CK2M032WAMH-A	TO-247-4L	1CK2M032WAMH	Tube	See the detail package information

Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	1200	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) ^(Note 1)	80	A
	Drain Current - Continuous ($T_C = 100^\circ\text{C}$) ^(Note 1)	56.5	A
I_{DM}	Drain Current - Pulsed ^(Note 2)	120	A
V_{GS}	Gate - Source Voltage (dynamic)	-10/+22	V
V_{GS}	Gate - Source Voltage (static)	-6/+18	V
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	348	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction - to - Case, Steady - State	0.43	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction - to - Ambient, Steady - State	40	°C/W

Notes:

1. The max drain current limited by maximum junction temperature
2. Repetitive Rating: Pulse width limited by maximum junction temperature

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Static Characteristics						
$V_{(\text{BR})\text{DSS}}$	Drain - Source Breakdown Voltage	$V_{GS} = 0 \text{ V}$, $I_D = 100 \mu\text{A}$	1200			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 1200 \text{ V}$, $V_{GS} = 0 \text{ V}$		5	50	μA
I_{GSS}	Gate Leakage Current	$V_{GS} = + 22 \text{ V}$, $V_{DS} = 0 \text{ V}$			100	nA
		$V_{GS} = - 10 \text{ V}$, $V_{DS} = 0 \text{ V}$			-100	nA
$V_{GS(\text{th})}$	Gate Threshold voltage	$V_{DS} = V_{GS}$, $I_D = 11.5 \text{ mA}$	2.2	3.2	4.2	V
		$V_{DS} = V_{GS}$, $I_D = 11.5 \text{ mA}$, $T_J = 175^\circ\text{C}$		2.3		V
$R_{\text{DS(ON)}}$	Drain-Source on-state resistance	$V_{GS} = 18 \text{ V}$, $I_D = 40 \text{ A}$		27	38	$\text{m}\Omega$
		$V_{GS} = 18 \text{ V}$, $I_D = 40 \text{ A}$, $T_J = 175^\circ\text{C}$		45		$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = 20 \text{ V}$, $I_D = 40 \text{ A}$		20		S
		$V_{DS} = 20 \text{ V}$, $I_D = 40 \text{ A}$, $T_J = 175^\circ\text{C}$		21		S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 800 \text{ V}$, $V_{GS} = 0 \text{ V}$, $F = 100 \text{ kHz}$, $V_{AC} = 25 \text{ mV}$		3550		pF
C_{oss}	Output Capacitance			130		pF
C_{rss}	Reverse Transfer Capacitance			10		pF
E_{oss}	C_{oss} Stored Energy			55		μJ
R_g	Gate Resistance	$F = 1 \text{ MHz}$, $V_{AC} = 25 \text{ mV}$		1.2		Ω
Q_{gs}	Gate-Source Charge	$V_{DS} = 800 \text{ V}$, $I_D = 35 \text{ A}$, $V_{GS} = -5/+18 \text{ V}$		45		nC
Q_{gd}	Gate-Drain Charge			62		nC
Q_g	Total Gate Charge			176		nC

Switching Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$t_{d(on)}$	Turn On Delay Time	$V_{DD} = 800 \text{ V}$, $I_D = 40 \text{ A}$, $V_{GS} = -4/+18 \text{ V}$, $R_{G,EXT} = 10 \Omega$ $L = 100 \mu\text{H}$ Diode: Body Diode at $V_{GS} = -4\text{V}$		17		ns
t_r	Rise Time			37		ns
$t_{d(off)}$	Turn Off Delay Time			56		ns
t_f	Fall Time			20		ns
E_{on}	Turn On Energy			850		μJ
E_{off}	Turn Off Energy			370		μJ
$t_{d(on)}$	Turn On Delay Time	$V_{DD} = 800 \text{ V}$, $I_D = 40 \text{ A}$, $V_{GS} = -4/+18 \text{ V}$, $R_{G,EXT} = 10 \Omega$ $L = 100 \mu\text{H}$ Diode: Body Diode at $V_{GS} = -4\text{V}$ $T_J = 175 \text{ }^\circ\text{C}$		11		ns
t_r	Rise Time			36		ns
$t_{d(off)}$	Turn Off Delay Time			75		ns
t_f	Fall Time			23		ns
E_{on}	Turn On Energy			1720		μJ
E_{off}	Turn Off Energy			480		μJ

Drain-Source Diode Characteristics ($T_J = 25 \text{ }^\circ\text{C}$ unless otherwise noted)

I_S	Maximum Continuous Drain-Source Diode Forward Current			80	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current			120	A
V_{SD}	Diode Forward Voltage	$V_{GS} = -4 \text{ V}$, $I_{SD} = 20 \text{ A}$		5	V
		$V_{GS} = -4 \text{ V}$, $I_{SD} = 20 \text{ A}$, $T_J = 175 \text{ }^\circ\text{C}$		4.3	V
I_{rrm}	Peak Reverse Recovery Current	$V_{GS} = -4 \text{ V}$, $I_{SD} = 40 \text{ A}$, $V_R = 800 \text{ V}$, $di/dt = 2660 \text{ A}/\mu\text{s}$		27	A
t_{rr}	Reverse Recovery Time			18	ns
Q_{rr}	Reverse Recovery Charge			293	nC

Electrical Characteristics Diagrams

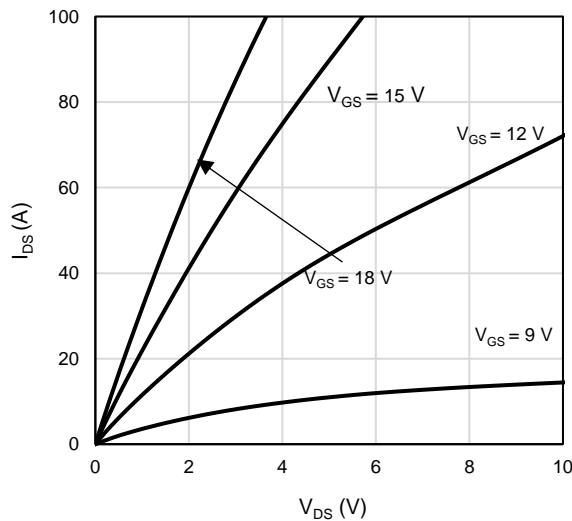
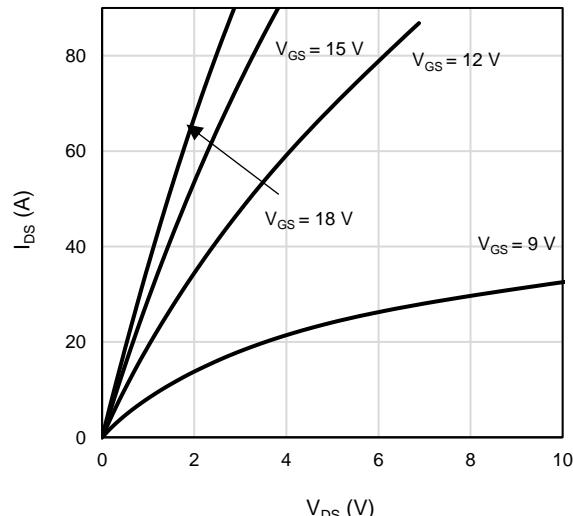
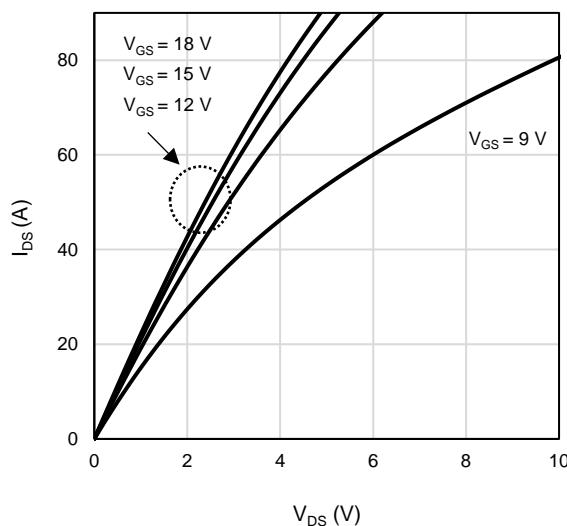
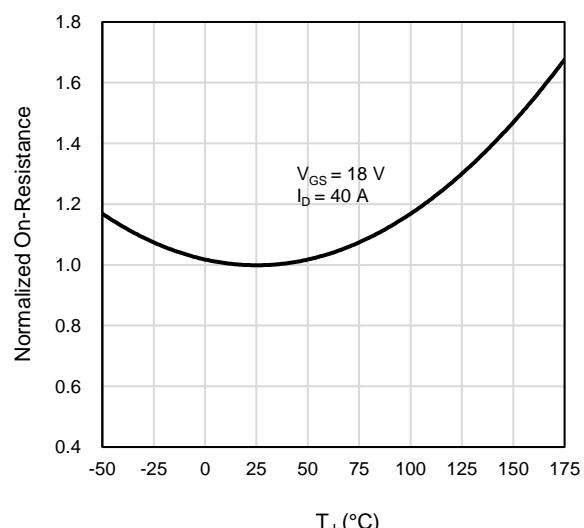
Figure 1: Output Characteristics $T_J = -40^\circ\text{C}$ Figure 2: Output Characteristics $T_J = 25^\circ\text{C}$ Figure 3: Output Characteristics $T_J = 175^\circ\text{C}$ 

Figure 4: Normalized On-Resistance vs. Temperature

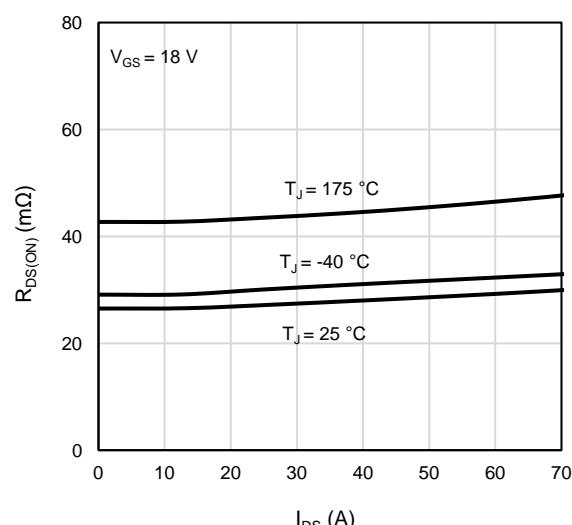


Figure 5: On-Resistance vs. Drain Current For Various Temperatures

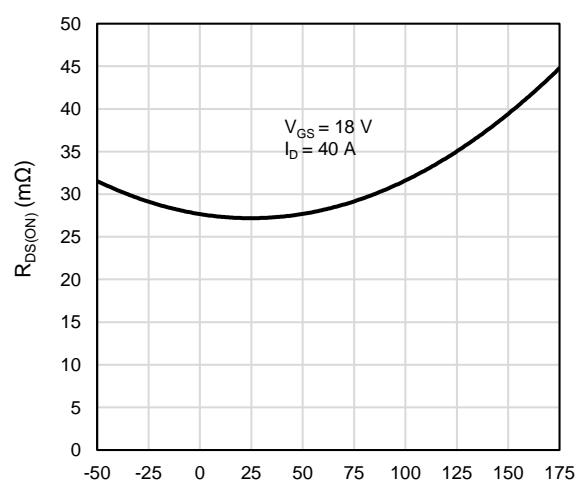


Figure 6: On-Resistance vs. Temperature

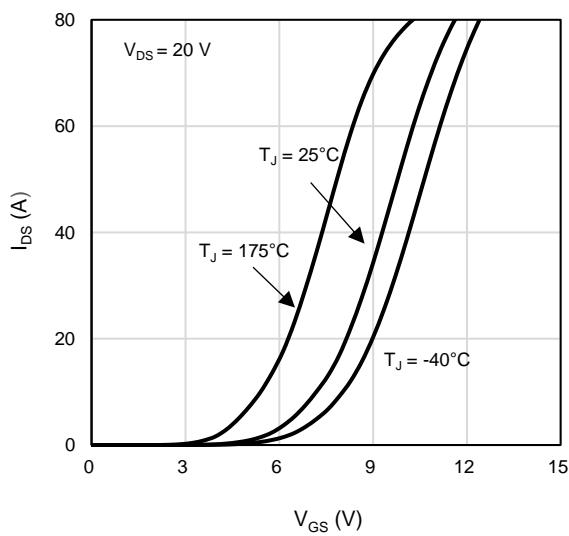


Figure 7: Transfer Characteristics For Various Junction Temperature

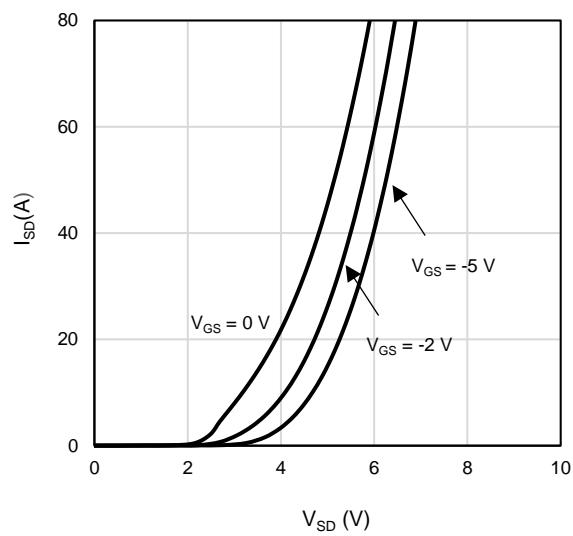
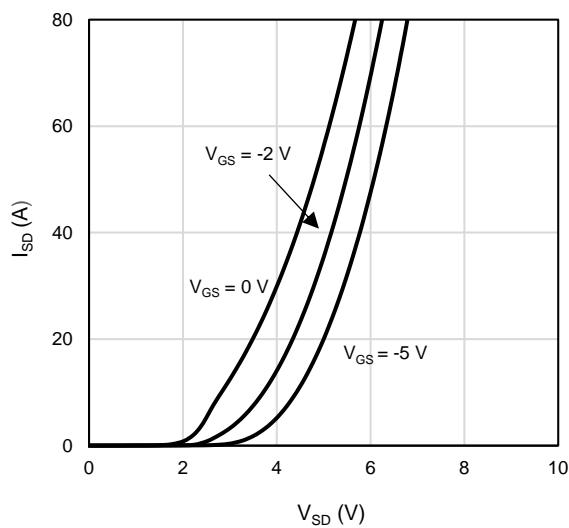
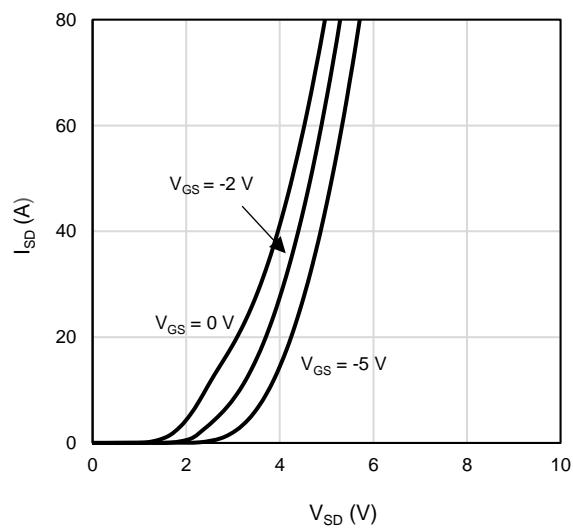
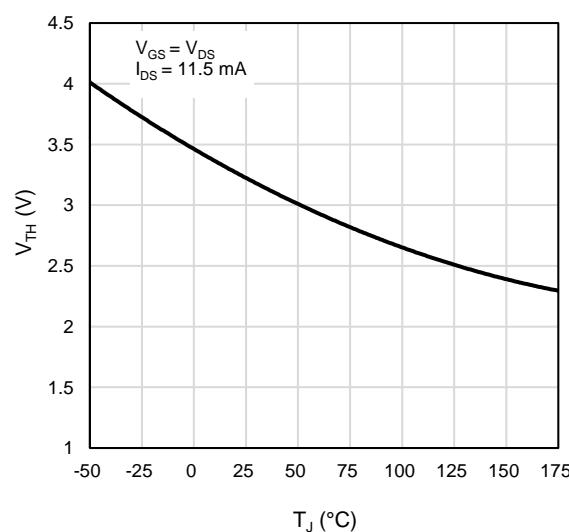
Figure 8: Body Diode Characteristics at -40°C Figure 9: Body Diode Characteristics at 25°C Figure 10: Body Diode Characteristics at 175°C 

Figure 11: Threshold Voltage vs. Temperature

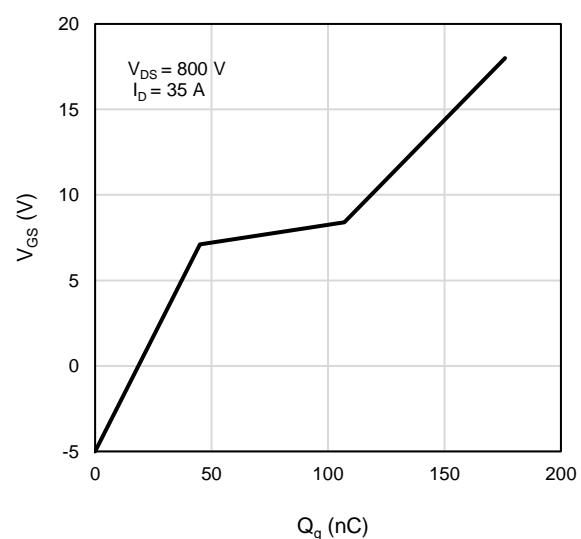


Figure 12: Gate-Charge Characteristics

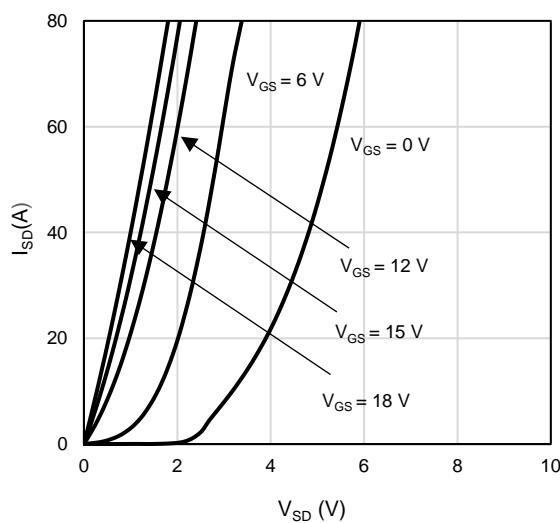
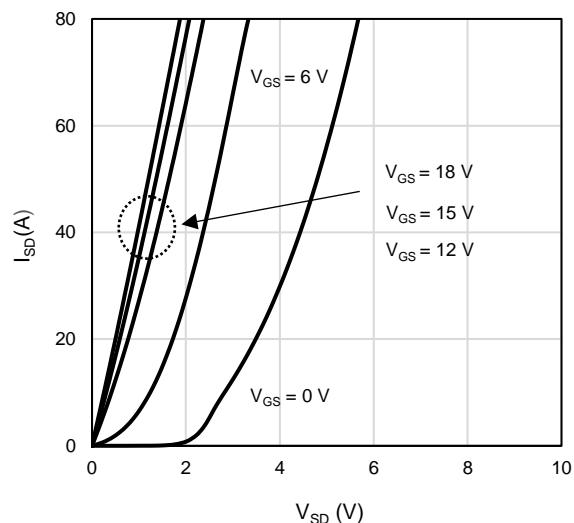
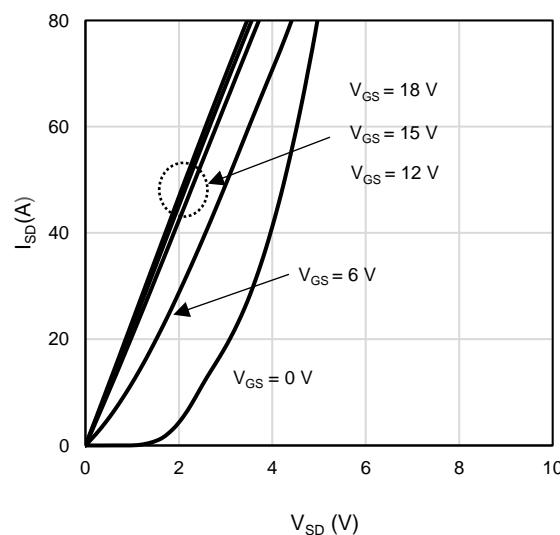
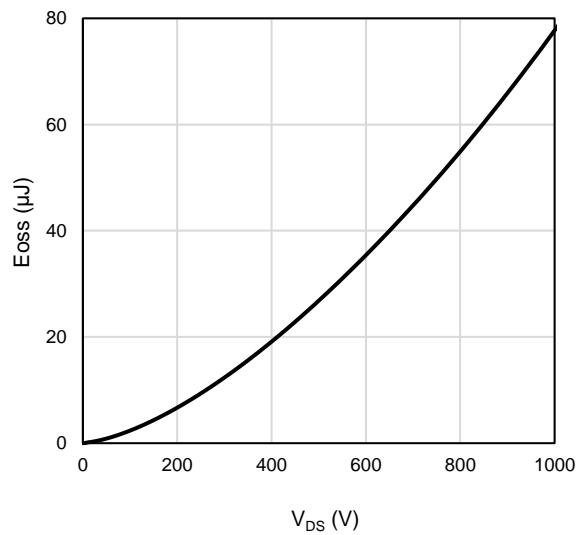
Figure 13: 3rd Quadrant Characteristics at -40°C Figure 14: 3rd Quadrant Characteristics at 25°C Figure 15: 3rd Quadrant Characteristics at 175°C 

Figure 16: Output Capacitor Stord Energy

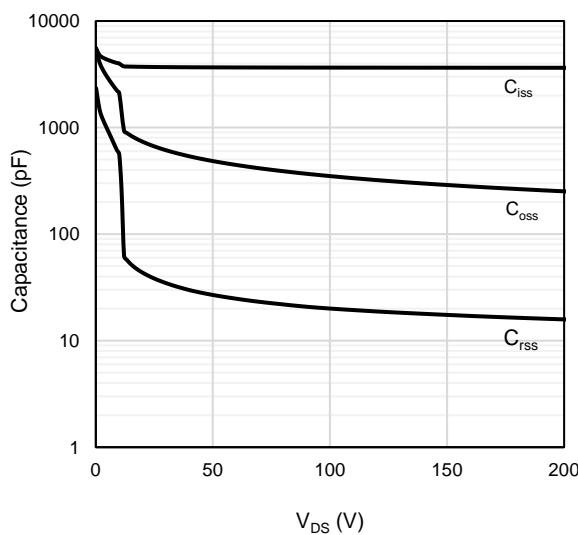


Figure 17: Capacitance Characteristics (0 - 200 V)

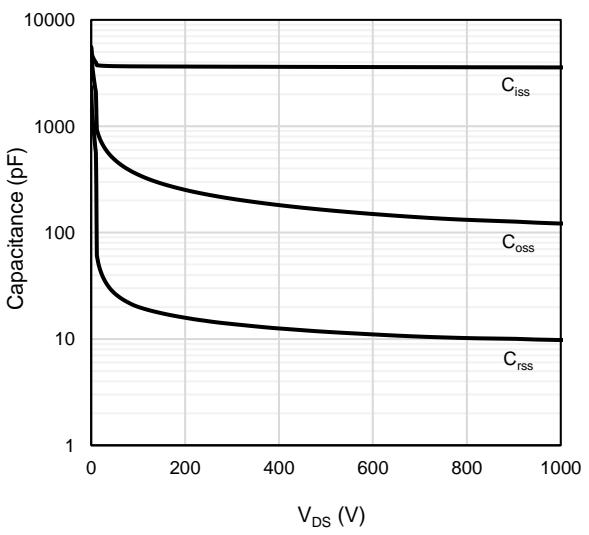


Figure 18: Capacitance Characteristics (0 - 1000 V)

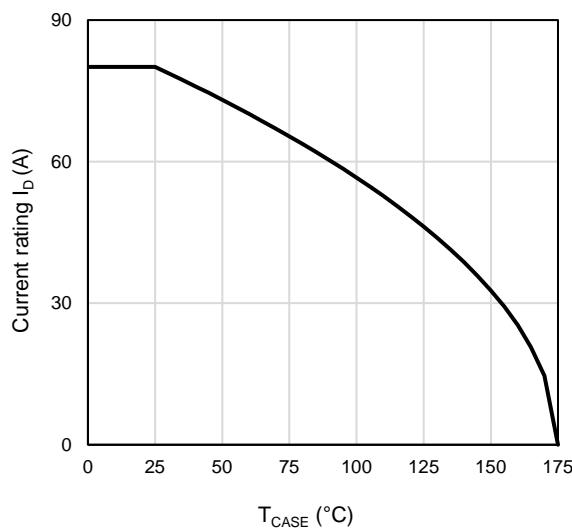


Figure 19: Current De-rating

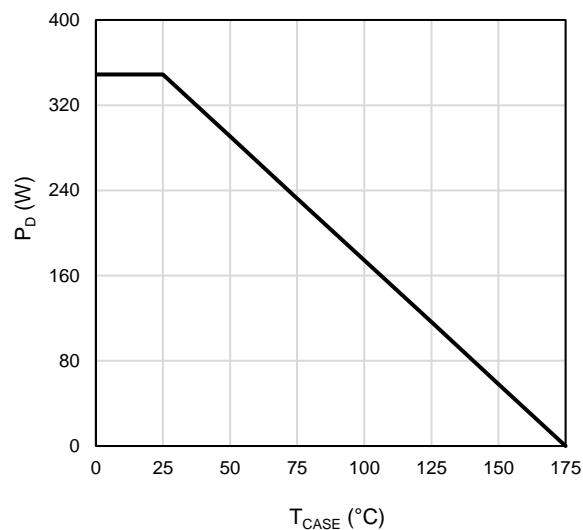


Figure 20: Maximum Power Dissipation Derating vs. CaseTemperature

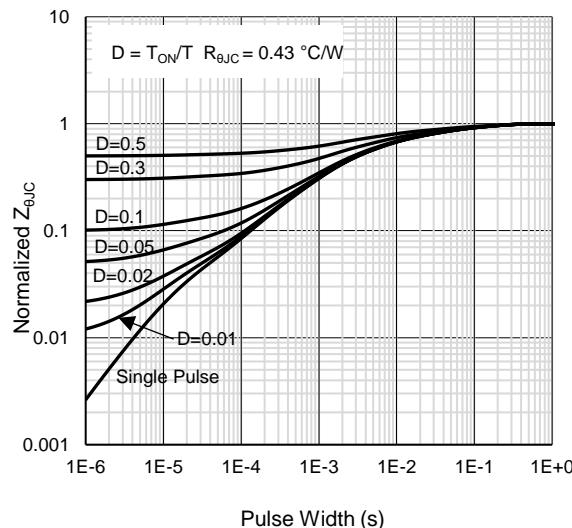


Figure 21: Normalized Maximum Transient Thermal Impedance

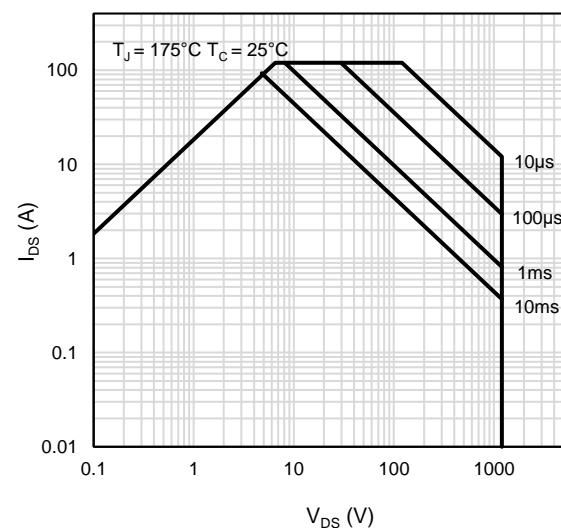
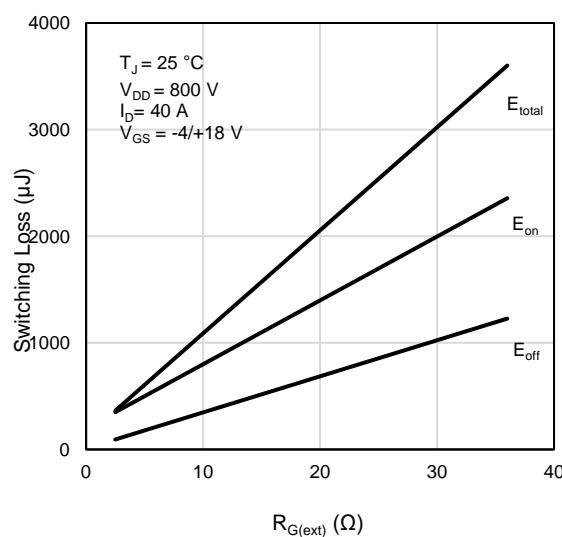
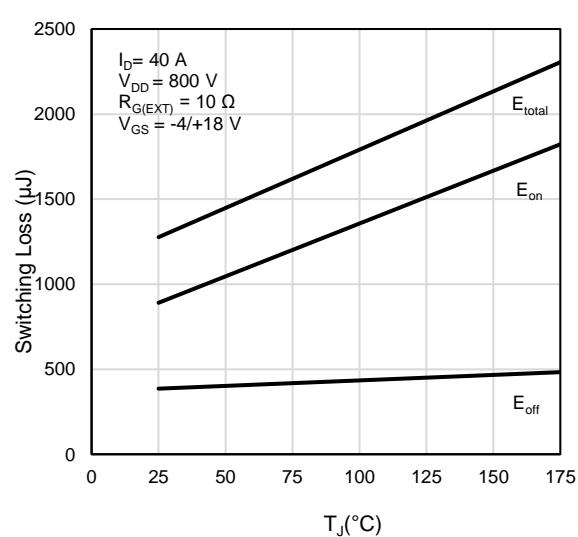
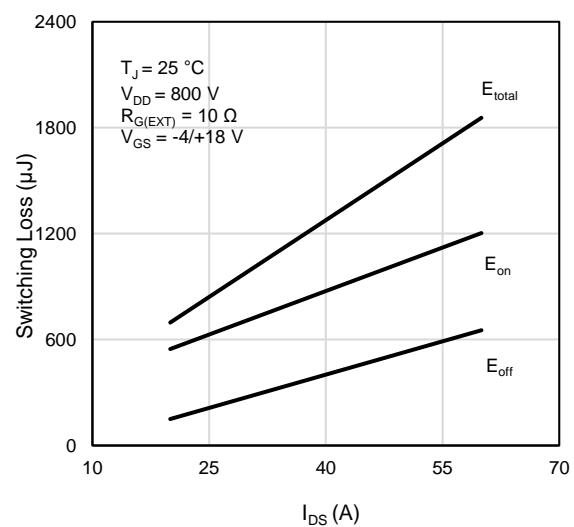
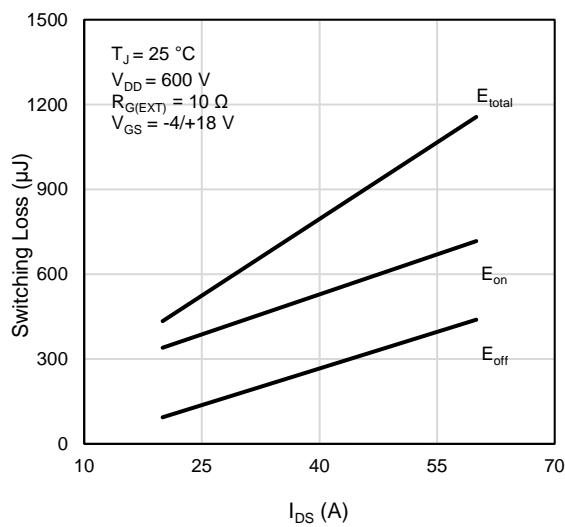


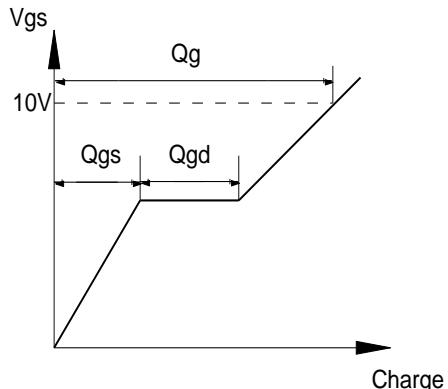
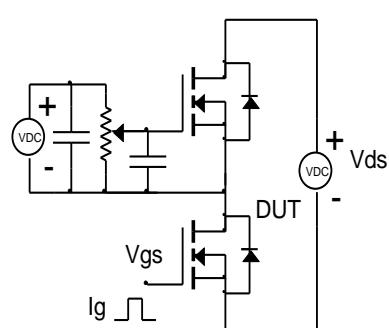
Figure 22: Maximum Forward Biased Safe Operating Area

Figure 23: Clamped Inductive Switching Energy vs. R_{G(ext)}Figure 24: Clamped Inductive Switching Energy vs. T_J

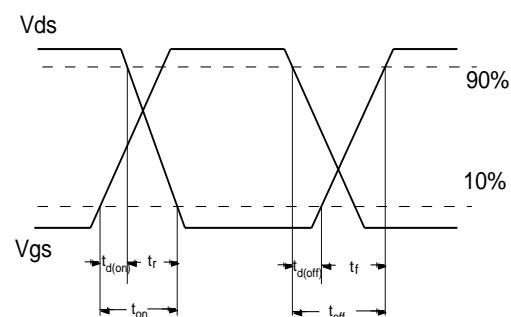
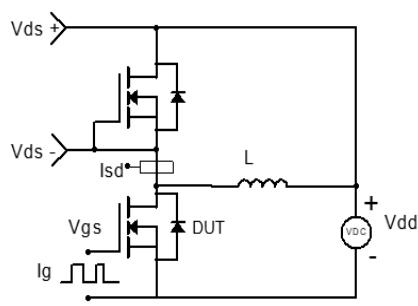


Test Circuit and Waveform

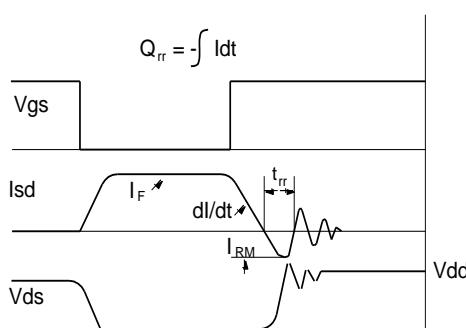
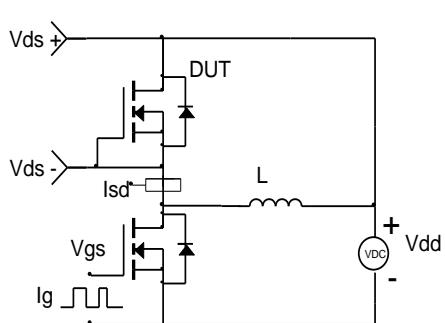
Gate Charge Test Circuit & Waveform



Clamped Inductive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Revision History

Revision	Released	Remark
Rev.1.0	2024	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.