

1200V 75mohm Silicon Carbide Power MOSFET
AK1CK2M075WAMH

Features:

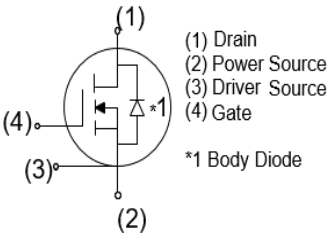
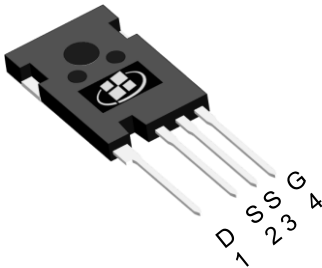
- Low on-resistance
- Fast switching speed with low capacitances
- Fast intrinsic diode with low reverse recovery (Q_{rr})
- Halogen-free, RoHS compliant

Applications:

- Motor drives
- Switched mode power supplies

Key Performance Parameters:

Parameter	Value	Unit
V_{DS}	1200	V
$R_{DS(on), TYP} @V_{GS} = 18\text{ V}$	75	mΩ
I_D	29	A
P_D	136	W



Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AK1CK2M075WAMH	TO-247-4L	1CK2M075WAMH	Tube	See the detail package information

Maximum Ratings (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Value	Units
V _{DS}	Drain-Source Voltage	1200	V
I _D	Drain Current - Continuous (T _C = 25°C) ^(Note 1)	29	A
	Drain Current - Continuous (T _C = 100°C) ^(Note 1)	20	A
I _{DM}	Drain Current - Pulsed ^(Note 2)	72.5	A
V _{GS}	Gate - Source Voltage (dynamic)	-10/+22	V
V _{GS}	Gate - Source Voltage (static)	-6/+18	V
P _D	Power Dissipation (T _C = 25°C)	136	W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +175	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
R _{θJC}	Thermal Resistance, Junction - to - Case, Steady - State	1.1	°C/W
R _{θJA}	Thermal Resistance, Junction - to - Ambient, Steady - State	40	°C/W

Notes:

1. The max drain current limited by maximum junction temperature
2. Repetitive Rating: Pulse width limited by maximum junction temperature

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Static Characteristics						
V _{(BR)DSS}	Drain - Source Breakdown Voltage	V _{GS} = 0 V, I _D = 100 μA	1200			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 1200 V, V _{GS} = 0 V		5	50	μA
I _{GSS}	Gate Leakage Current	V _{GS} = + 22 V, V _{DS} = 0 V			100	nA
		V _{GS} = - 10 V, V _{DS} = 0 V			-100	nA
V _{GS(th)}	Gate Threshold voltage	V _{DS} = V _{GS} , I _D = 5 mA	1.8	2.6	4	V
		V _{DS} = V _{GS} , I _D = 5 mA, T _J = 175 °C		1.8		V
R _{DS(ON)}	Drain-Source on-state resistance	V _{GS} = 18 V, I _D = 20 A		75	90	mΩ
		V _{GS} = 18 V, I _D = 20 A, T _J = 175 °C		135		mΩ
g _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 20 A		13		S
		V _{DS} = 20 V, I _D = 20 A, T _J = 175 °C		11		S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 800 V, V _{GS} = 0 V, F = 100 kHz, V _{AC} = 25 mV		1268		pF
C _{oss}	Output Capacitance			48		pF
C _{rss}	Reverse Transfer Capacitance			4		pF
E _{oss}	C _{oss} Stored Energy			20		μJ
R _g	Gate Resistance	F = 1 MHz, V _{AC} = 25 mV		2.5		Ω
Q _{gs}	Gate-Source Charge	V _{DS} = 800 V , I _D = 20 A, V _{GS} = -4/+18 V		14		nC
Q _{gd}	Gate-Drain Charge			28		nC
Q _g	Total Gate Charge			65		nC

Switching Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$t_{d(on)}$	Turn On Delay Time	$V_{DD} = 800\text{ V}$, $I_D = 20\text{ A}$, $V_{GS} = -4/+18\text{ V}$, $R_{G,EXT} = 5\ \Omega$ $L = 100\ \mu\text{H}$ Diode: Body Diode at $V_{GS} = -4\text{V}$		9		ns
t_r	Rise Time			20		ns
$t_{d(off)}$	Turn Off Delay Time			33		ns
t_f	Fall Time			11		ns
E_{on}	Turn On Energy			342		μJ
E_{off}	Turn Off Energy			65		μJ
$t_{d(on)}$	Turn On Delay Time	$V_{DD} = 800\text{ V}$, $I_D = 20\text{ A}$, $V_{GS} = -4/+18\text{ V}$, $R_{G,EXT} = 20\ \Omega$ $L = 100\ \mu\text{H}$ Diode: Body Diode at $V_{GS} = -4\text{V}$		18		ns
t_r	Rise Time			36		ns
$t_{d(off)}$	Turn Off Delay Time			61		ns
t_f	Fall Time			25		ns
E_{on}	Turn On Energy			492		μJ
E_{off}	Turn Off Energy			196		μJ

Drain-Source Diode Characteristics ($T_J = 25\text{ }^\circ\text{C}$ unless otherwise noted)

I_S	Maximum Continuous Drain-Source Diode Forward Current			29	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current			72.5	A
V_{SD}	Diode Forward Voltage	$V_{GS} = -4\text{ V}$, $I_{SD} = 10\text{ A}$		4.8	V
		$V_{GS} = -4\text{ V}$, $I_{SD} = 10\text{ A}$, $T_J = 175\text{ }^\circ\text{C}$		4.2	V
I_{rrm}	Peak Reverse Recovery Current	$V_{GS} = -4\text{ V}$, $I_{SD} = 20\text{ A}$, $V_R = 800\text{ V}$, $di/dt = 1950\text{ A}/\mu\text{s}$		27	A
t_{rr}	Reverse Recovery Time			14	ns
Q_{rr}	Reverse Recovery Charge			193	nC

Electrical Characteristics Diagrams

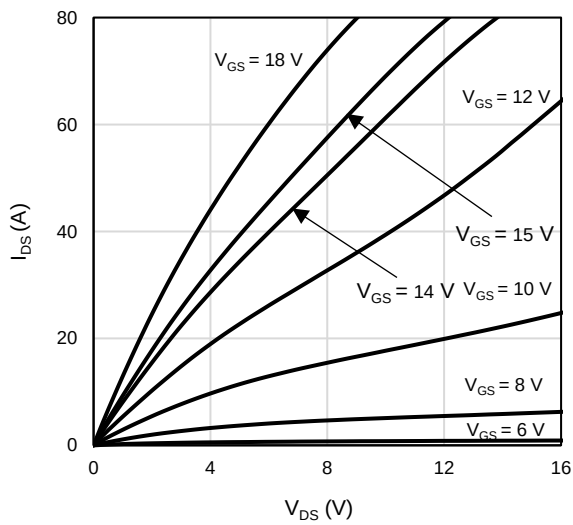
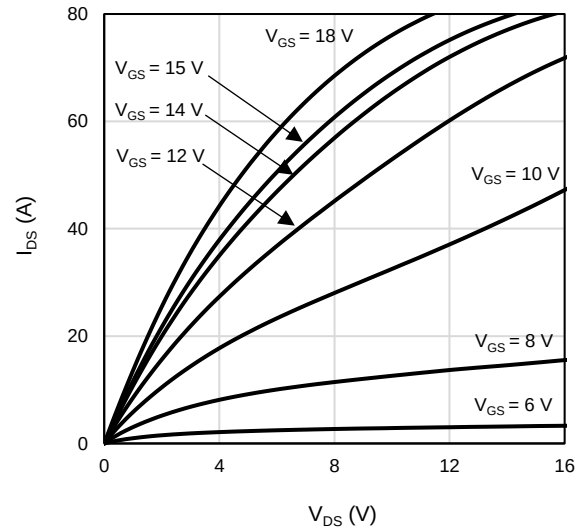
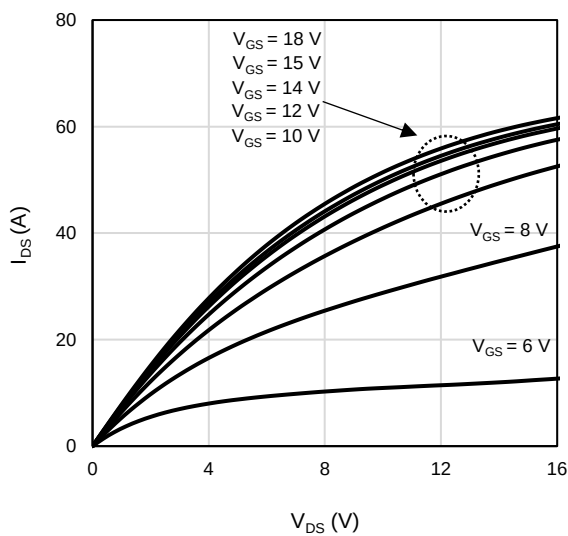
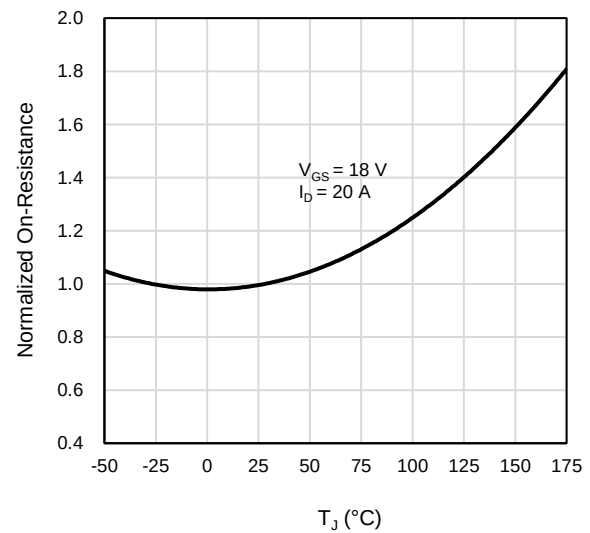
Figure 1: Output Characteristics $T_J = -55^{\circ}\text{C}$ Figure 2: Output Characteristics $T_J = 25^{\circ}\text{C}$ Figure 3: Output Characteristics $T_J = 175^{\circ}\text{C}$ 

Figure 4: Normalized On-Resistance vs. Temperature

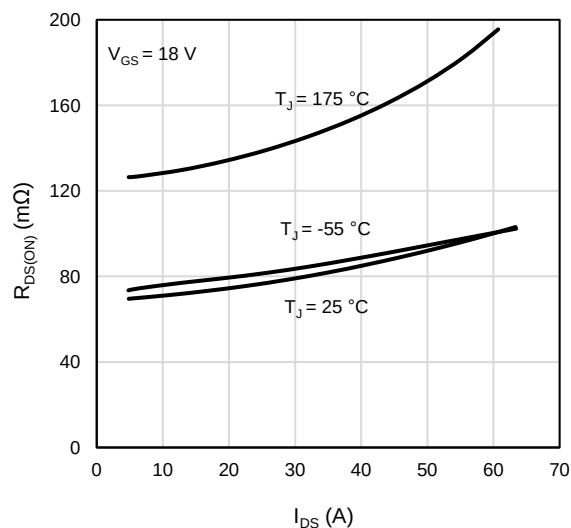
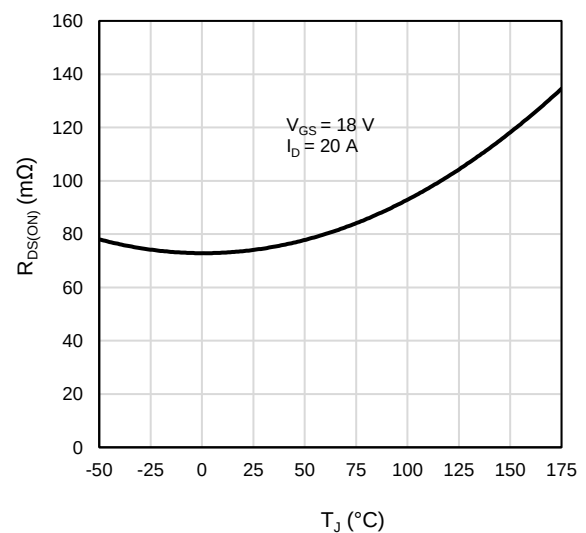
Figure 5: On-Resistance vs. Drain Current
For Various Temperatures

Figure 6: On-Resistance vs. Temperature

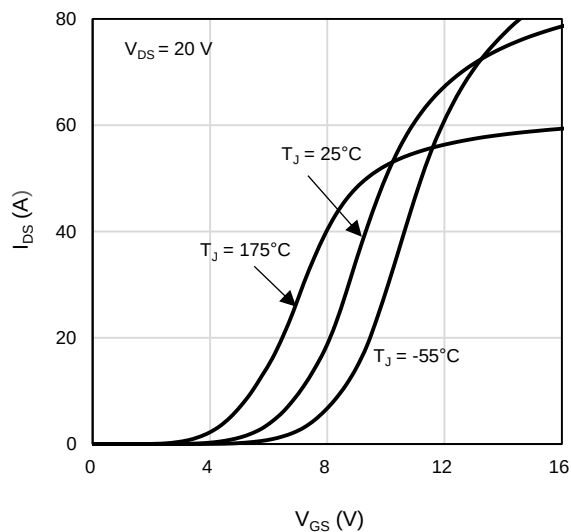


Figure 7: Transfer Characteristics For Various Junction Temperature

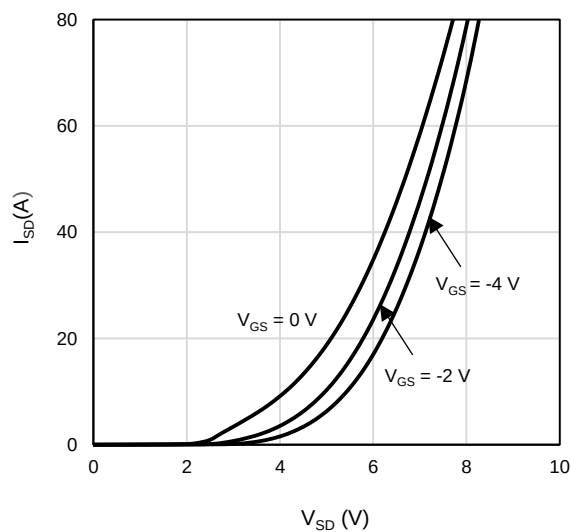


Figure 8: Body Diode Characteristics at -55°C

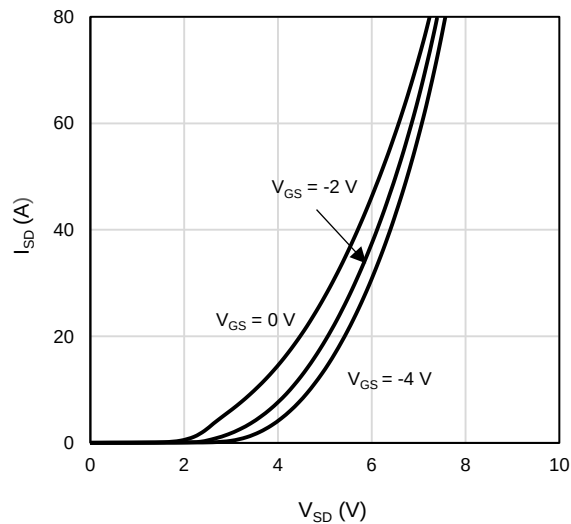


Figure 9: Body Diode Characteristics at 25°C

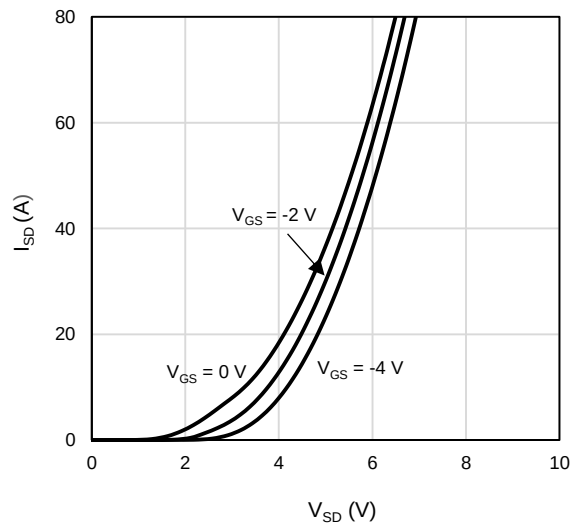


Figure 10: Body Diode Characteristics at 175°C

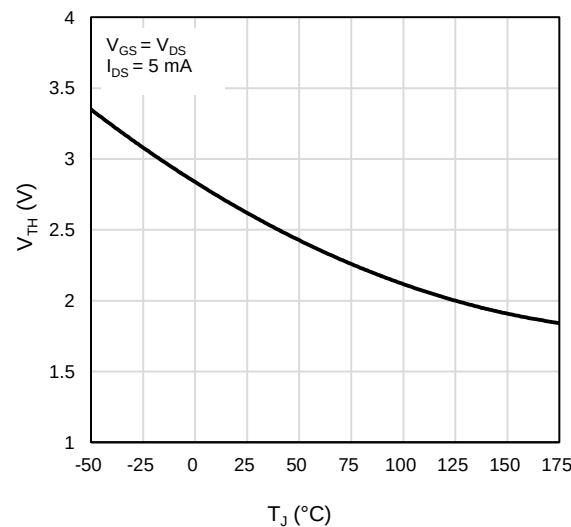


Figure 11: Threshold Voltage vs. Temperature

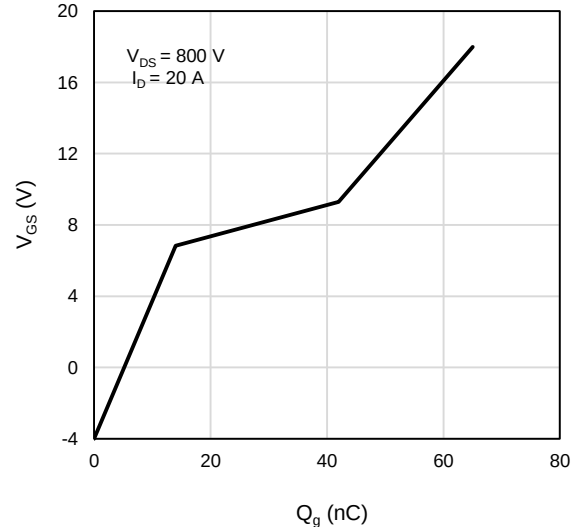


Figure 12: Gate-Charge Characteristics

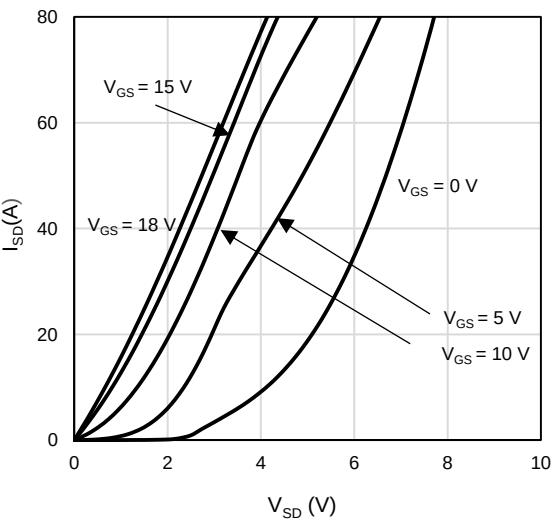


Figure 13: 3rd Quadrant Characteristics at -55°C

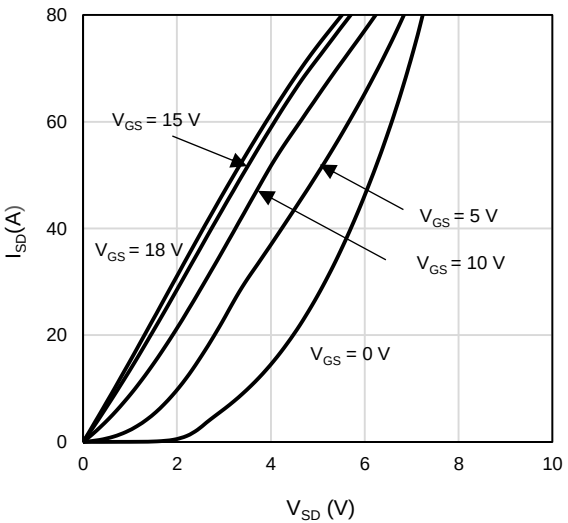


Figure 14: 3rd Quadrant Characteristics at 25°C

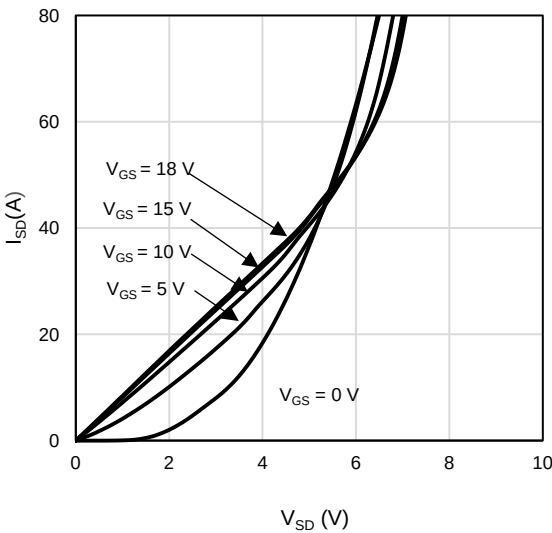


Figure 15: 3rd Quadrant Characteristics at 175°C

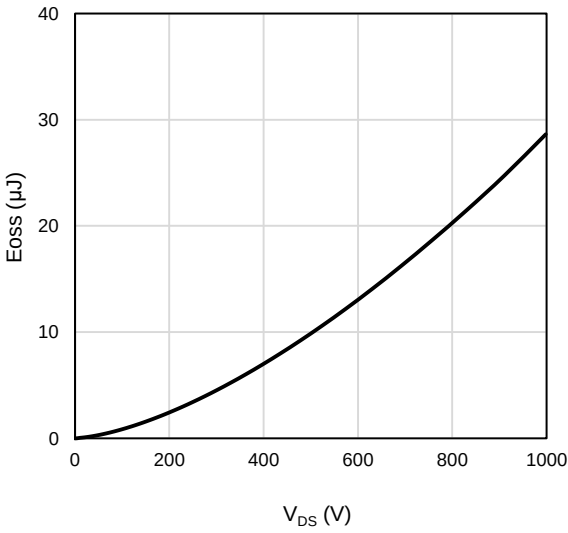


Figure 16: Output Capacitor Stord Energy

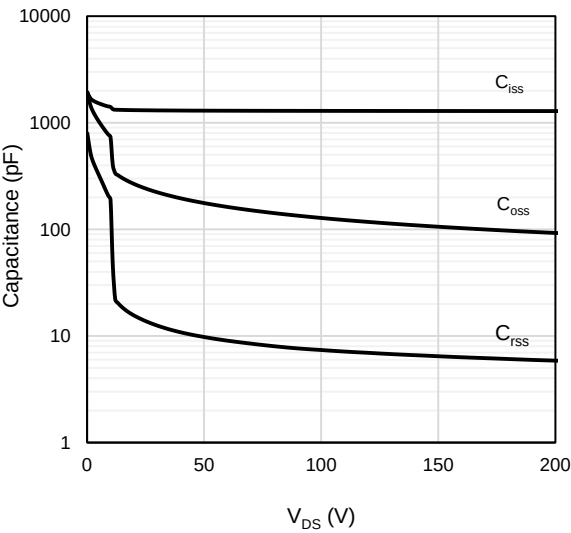


Figure 17: Capacitance Characteristics (0 - 200 V)

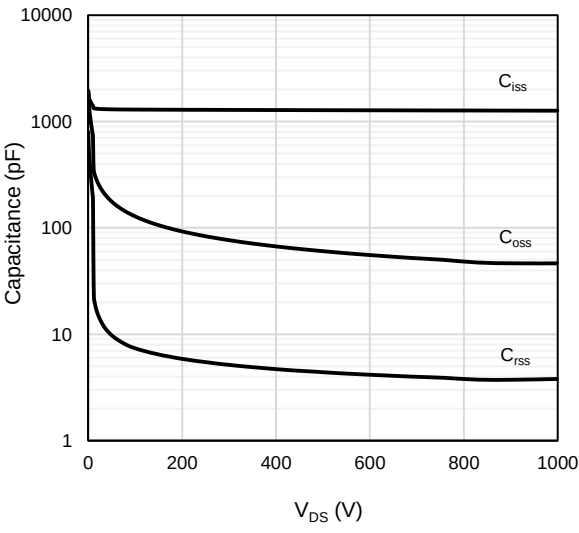


Figure 18: Capacitance Characteristics (0 - 1000 V)

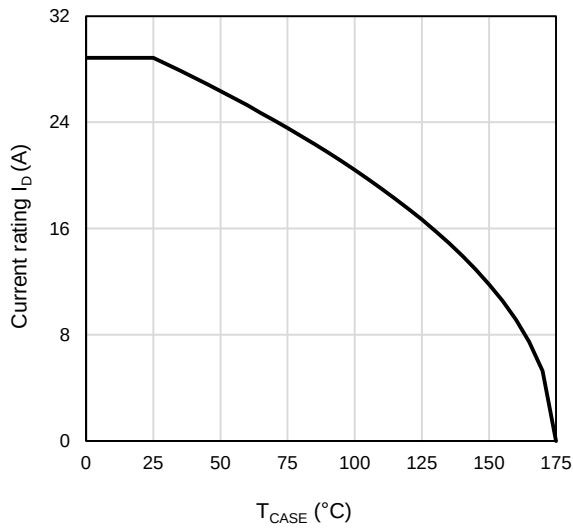


Figure 19: Current De-rating

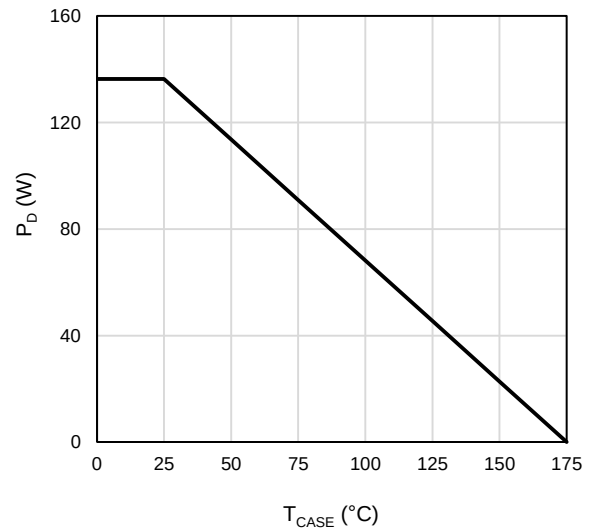


Figure 20: Maximum Power Dissipation Derating vs. Case Temperature

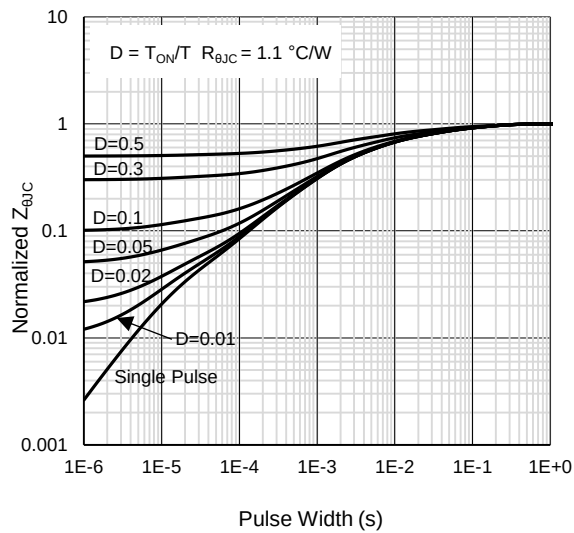


Figure 21: Normalized Maximum Transient Thermal Impedance

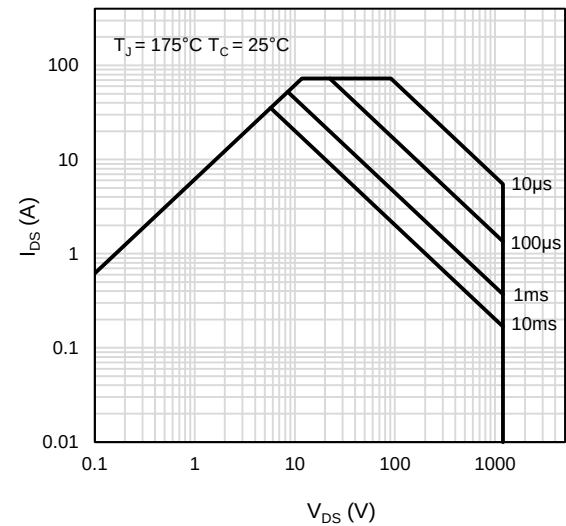
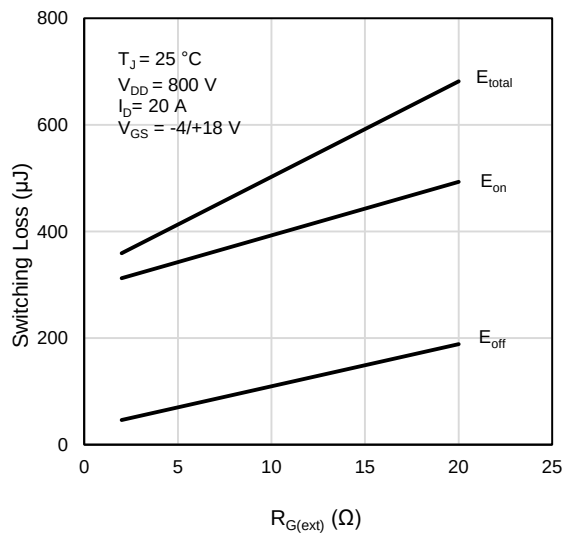
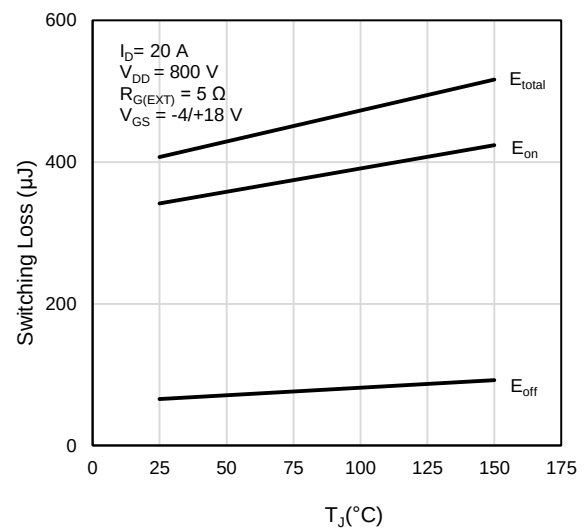


Figure 22: Maximum Forward Biased Safe Operating Area

Figure 23: Clamped Inductive Switching Energy vs. $R_{G(ext)}$ Figure 24: Clamped Inductive Switching Energy vs. T_J

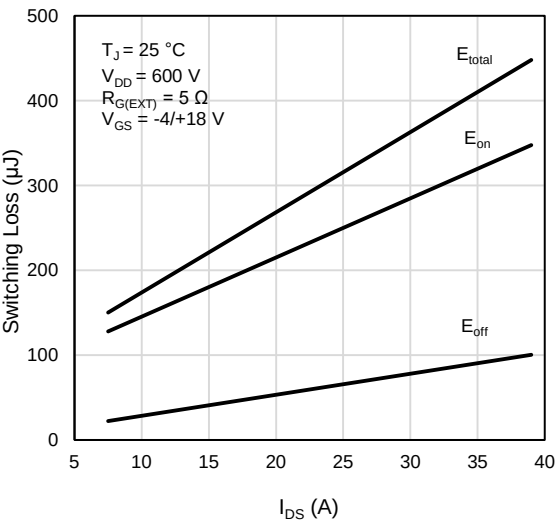


Figure 25: Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600\text{ V}$)

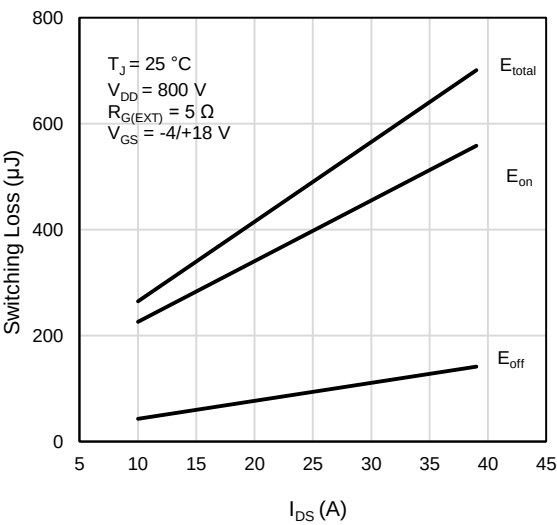


Figure 26: Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 800\text{ V}$)

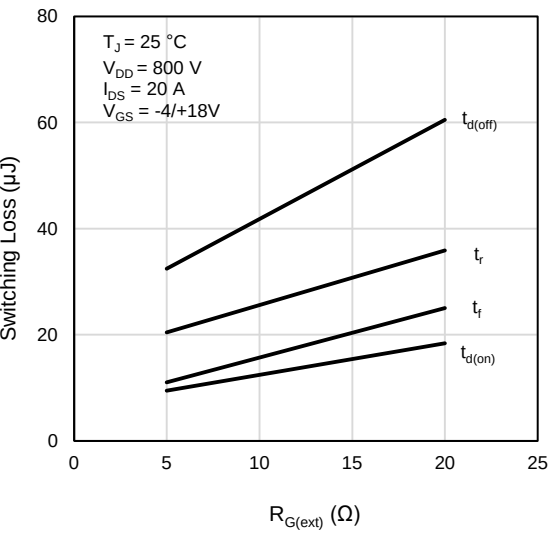
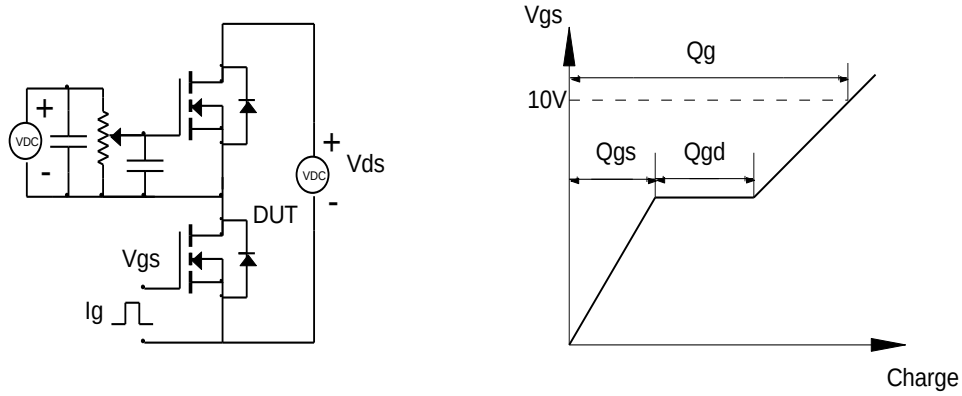


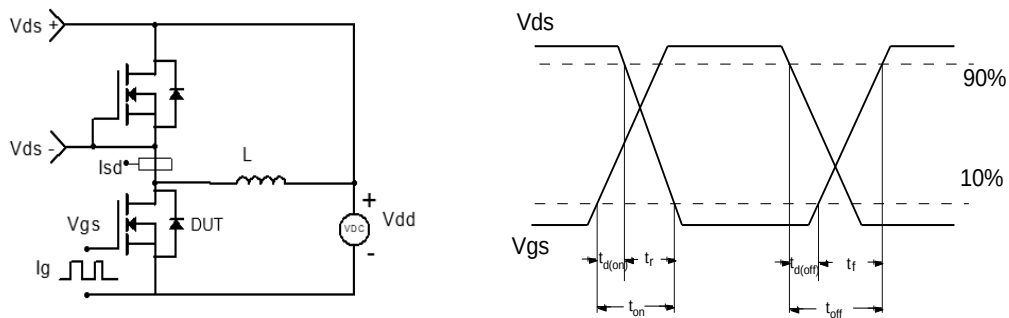
Figure 27: Switching Time vs. $R_{G(EXT)}$

Test Circuit and Waveform

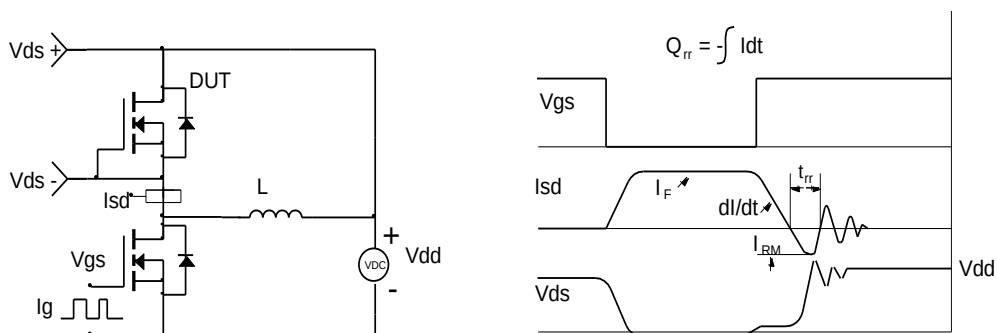
Gate Charge Test Circuit & Waveform



Clamped Inductive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Revision History		
Revision	Released	Remark
Rev.1.0	2024	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

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